



# N-Channel 40-V (D-S) MOSFET

PRODUCT SUMMARY			
$V_{DS}$ (V)	$r_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)	$Q_g$ (Typ)
40	0.040 at $V_{GS} = 10$ V	5.2	8
	0.045 at $V_{GS} = 4.5$ V	4.9	

## FEATURES

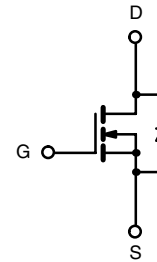
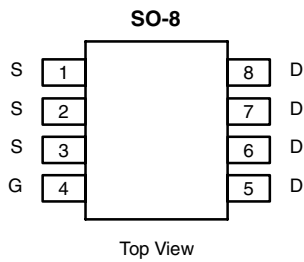
- TrenchFET® Power MOSFET
- 100 %  $R_g$  Tested
- UIS Tested

## APPLICATIONS

- CCFL Inverter



RoHS  
COMPLIANT



Ordering Information: Si4446DY-T1-E3 (Lead (Pb)-free)

ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)				
Parameter	Symbol	10 secs	Steady State	Unit
Drain-Source Voltage	$V_{DS}$	40		V
Gate-Source Voltage	$V_{GS}$	$\pm 12$		
Continuous Drain Current ( $T_J = 150^\circ\text{C}$ ) <sup>a</sup>	$I_D$	$T_A = 25^\circ\text{C}$	5.2	3.9
		$T_A = 70^\circ\text{C}$	4.2	3.1
Pulsed Drain Current	$I_{DM}$	30		A
Continuous Source Current (Diode Conduction) <sup>a</sup>	$I_S$	1.7	0.9	
Avalanche Current	$I_{AS}$	13		
Single Pulse Avalanche Energy	$E_{AS}$	8.5		mJ
Maximum Power Dissipation <sup>a</sup>	$P_D$	$T_A = 25^\circ\text{C}$	2.0	1.1
		$T_A = 70^\circ\text{C}$	1.3	0.7
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to 150		$^\circ\text{C}$

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Typ	Max	Unit
Maximum Junction-to-Ambient <sup>a</sup>	$R_{thJA}$	$t \leq 10$ sec	52	62.5
		Steady State	90	110
Maximum Junction-to-Foot (Drain)	$R_{thJF}$	32	40	$^\circ\text{C/W}$

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

SPECIFICATIONS (T <sub>J</sub> = 25 °C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	0.6		1.6	V
V <sub>DS</sub> Temperature Coefficient	ΔV <sub>DS/TJ</sub>	I <sub>D</sub> = 250 μA		40		mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	ΔV <sub>GS(th)/TJ</sub>			-3.8		
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ±12 V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V			1	μA
		V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ 5 V, V <sub>GS</sub> = 10 V	20			A
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5.2 A		0.033	0.040	Ω
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 4.9 A		0.037	0.045	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 5.2 A		18		S
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = 1.7 A, V <sub>GS</sub> = 0 V		0.75	1.2	V
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	C <sub>iss</sub>	N-Channel V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V, f = 1 MHz		700		pF
Output Capacitance	C <sub>oss</sub>			76		
Reverse Transfer Capacitance	C <sub>rss</sub>			45		
Total Gate Charge	Q <sub>g</sub>	N-Channel V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 5.2 A		8	12	nC
Gate-Source Charge	Q <sub>gs</sub>			1.5		
Gate-Drain Charge	Q <sub>gd</sub>			2.4		
Gate Resistance	R <sub>g</sub>		f = 1 MHz	1.9	2.9	
Turn-On Delay Time	t <sub>d(on)</sub>	N-Channel V <sub>DD</sub> = 15 V, R <sub>L</sub> = 15 Ω I <sub>D</sub> ≅ 1 A, V <sub>GEN</sub> = 10 V, R <sub>g</sub> = 6 Ω		7	11	ns
Rise Time	t <sub>r</sub>			11	17	
Turn-Off Delay Time	t <sub>d(off)</sub>			27	40	
Fall Time	t <sub>f</sub>			8	13	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>		I <sub>F</sub> = 1.7 A, di/dt = 100 A/μs		25	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 1.7 A, di/dt = 100 A/μs		17	26	nC

## Notes

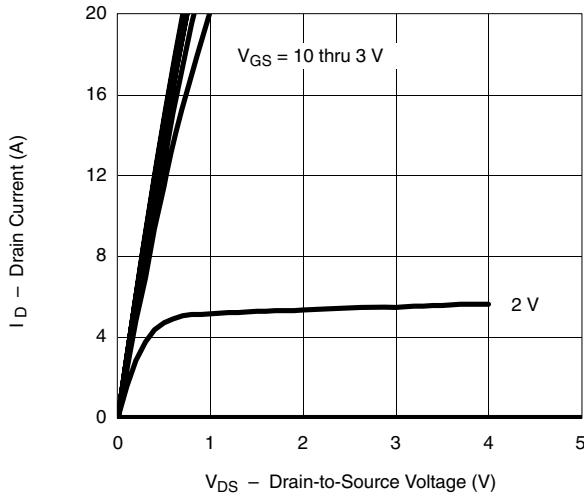
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.  
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

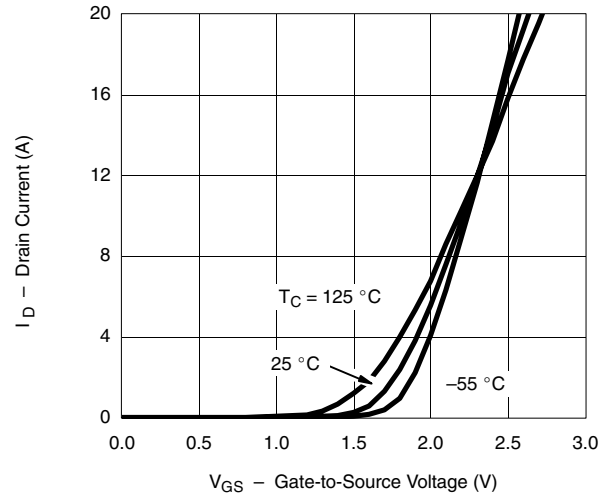


**TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)**

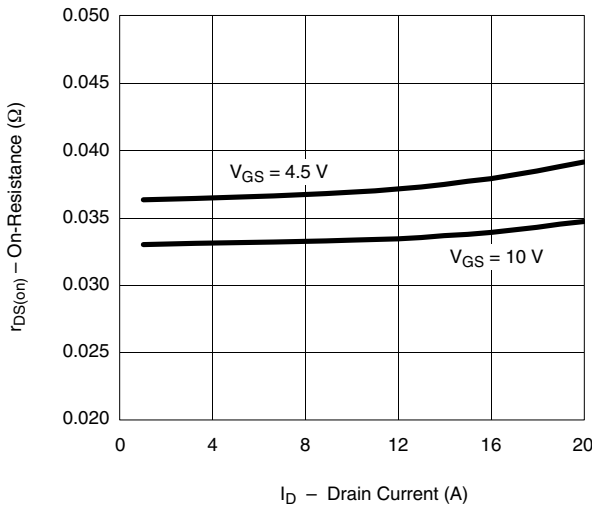
Output Characteristics



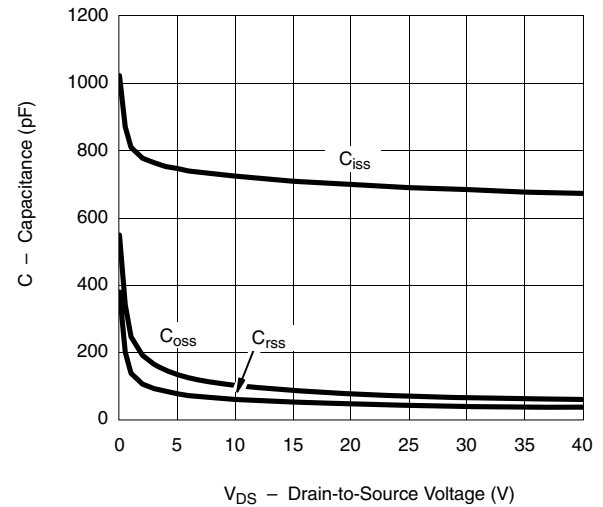
Transfer Characteristics



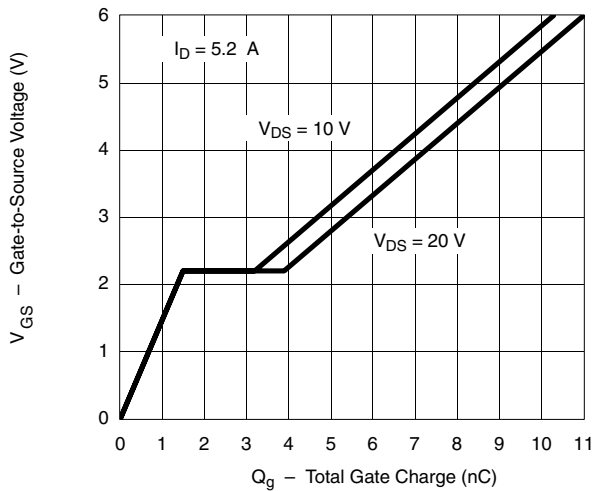
On-Resistance vs. Drain Current



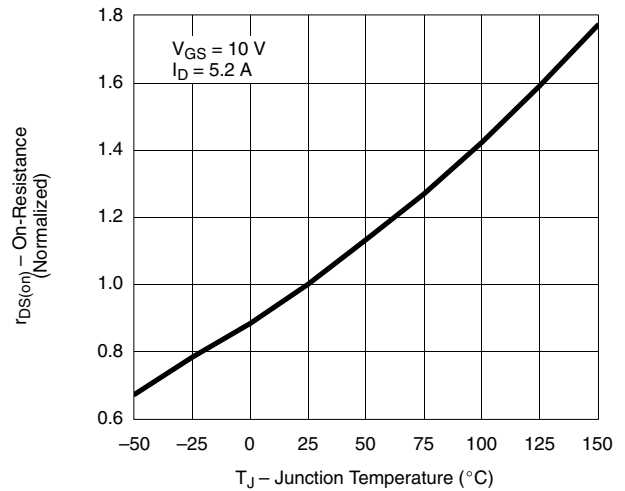
Capacitance



Gate Charge



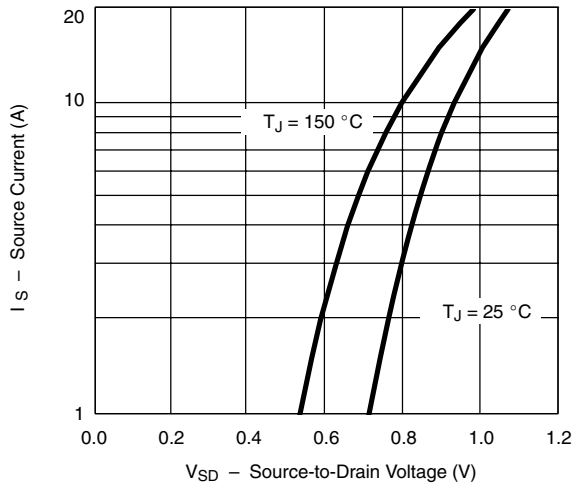
On-Resistance vs. Junction Temperature



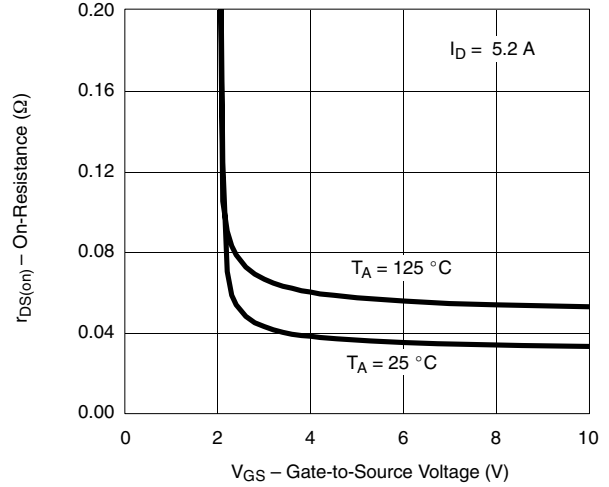


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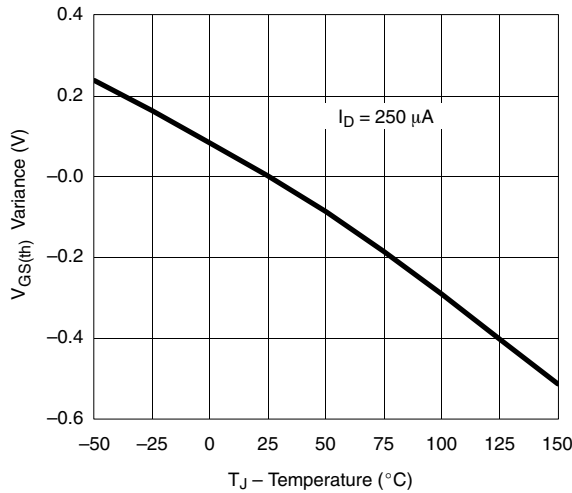
Source-Drain Diode Forward Voltage



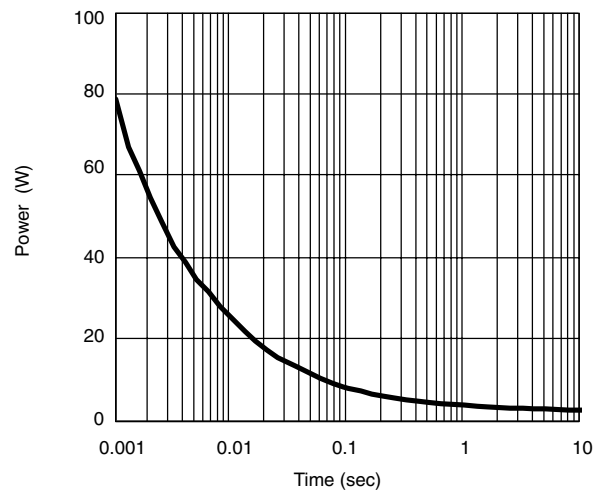
On-Resistance vs. Gate-to-Source Voltage



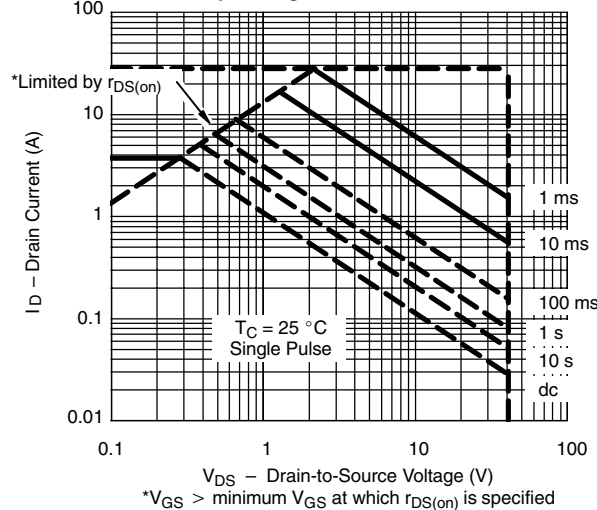
Threshold Voltage



Single Pulse Power, Junction-to-Ambient

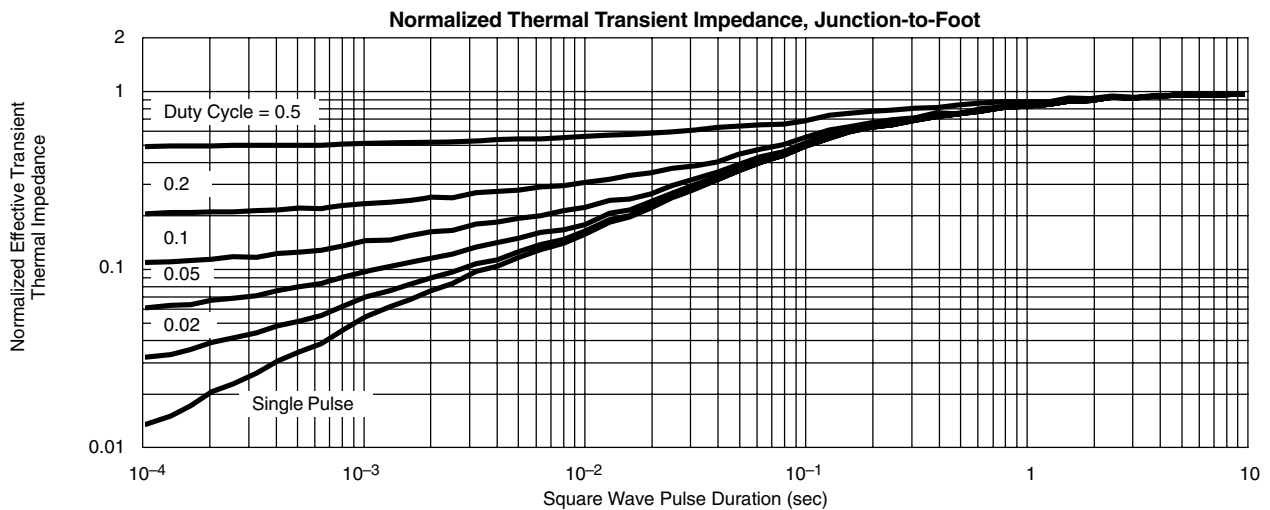
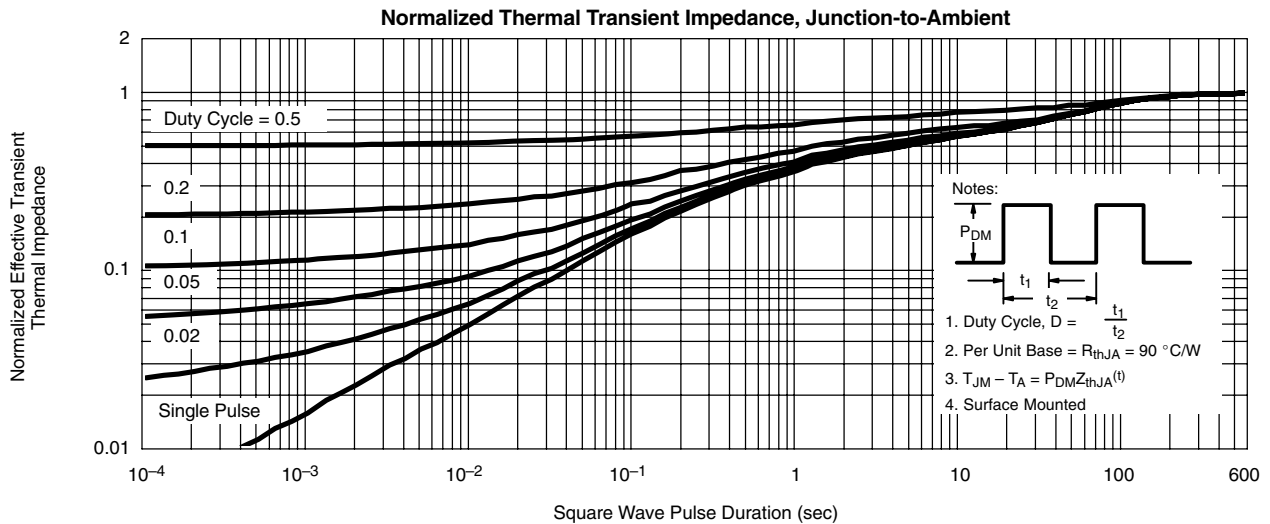


Safe Operating Area, Junction-to-Foot





**TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)**



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